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Zhen Chen
Gerald Hefferman
Tao Wei

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Zhen Chen,^a Gerald Hefferman,^{a,b} and Tao Wei^{a,*}

^aUniversity of Rhode Island, Department of Electrical, Computer, and Biomedical Engineering, Kingston, Rhode Island, United States

^bBrown University, Warren Alpert Medical School, Providence, Rhode Island, United States

Abstract. A field-programmable gate array (FPGA)-controlled sweep velocity-locked laser pulse generator (SV-LLPG) design based on an all-digital phase-locked loop (ADPLL) is proposed. A distributed feedback laser with modulated injection current was used as a swept-frequency laser source. An open-loop predistortion modulation waveform was calibrated using a feedback iteration method to initially improve frequency sweep linearity. An ADPLL control system was then implemented using an FPGA to lock the output of a Mach-Zehnder interferometer that was directly proportional to laser sweep velocity to an on-board system clock. Using this system, linearly chirped laser pulses with a sweep bandwidth of 111.16 GHz were demonstrated. Further testing evaluating the sensing utility of the system was conducted. In this test, the SV-LLPG served as the swept laser source of an optical frequency-domain reflectometry system used to interrogate a subterahertz range fiber structure (sub-THz-FS) array. A static strain test was then conducted and linear sensor results were observed. © 2017 Society of Photo-Optical Instrumentation Engineers (SPIE) [DOI: 10.1117/1.OE.56.5.054102]

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1 Introduction

Frequency-modulated continuous wave (FMCW) reflectometry or optical frequency-domain reflectometry (OFDR)¹⁻³ is a well-established frequency-domain measurement method for optical component characterization and optical fiber distributed sensing. This technique allows distance domain information to be obtained from frequency-domain intensity data via a Fourier transform. A key component of this FMCW reflectometry system is a swept-frequency laser source. A variety of laser sources have been investigated for this purpose, including temperature and piezo-electrically tuned Nd:YAG ring lasers,^{4,5} external cavity lasers (ECLs),⁶⁻¹³ piezo-electrically tuned fiber grating lasers,¹⁴ and chirped distributed feedback (DFB) lasers.¹⁵

Among these sources, the chirped DFB laser represents a particularly promising candidate for narrow-bandwidth interrogation applications. The output frequency of DFB lasers can be controlled using injection current modulation without the need for any moving mechanical components, resulting in a fast repetition rate over a sweeping bandwidth of ~100 GHz. Other beneficial features of chirped DFB lasers include single longitudinal mode output, good laser coherence length (~km), and low cost. However, there are several drawbacks limiting chirped DFB lasers as elements of FMCW/OFDR systems. Chief among these is a nonlinear relationship between input current and output frequency, leading to nonlinear optical sweep speeds. To compensate this nonlinearity, a predistortion waveform can be generated based on a feedback iteration method.¹⁶ However, this method only fractionally enhances the linearity of the resulting optical sweep speed. An auxiliary clock or “k-clock” with a fixed optical delay can also be applied to resample the data and correct for nonlinear sweep speeds.¹⁶ However,

this sampling clock method increases both system sampling and signal processing complexity.

A closed-loop control system based on an optical phase-locked loop (OPLL) that modifies laser output frequency in real time offers an alternative approach for precisely controlling optical sweep speed.^{17,18} Recently, a digital-controlled chirped pulse laser based on a digital phase-locked loop (DPLL) design was reported based on modular electronic design.¹⁹ A digital phase comparator (XOR gate) was utilized to extract phase errors between the output of a Mach-Zehnder interferometer (MZI), which converted laser sweep speed to a radio-frequency (RF) signal,^{20,21} and a reference oscillator. The system generated a highly linear frequency sweep, and its utility as a source for high spatial resolution fiber sensing applications was demonstrated. However, while this system successfully demonstrated the concept, there remain engineering challenges, stemming from the fact that analog systems are susceptible to noise and DC drifts in comparison to similar digital systems.^{22,23} More importantly, modular design results in relatively high-power consumption, large size, and weight of the final product.

This paper reports an alternative design for a sweep velocity-locked laser pulse generator (SV-LLPG) using an all-digital phase-lock loop (ADPLL), which has the potential to surmount several of the previous engineering challenges facing modular DPLL design. The ADPLL is constructed such that all components, including the phase comparator, loop controller, and reference frequency synthesizer, are digitally generated using logic gates and integrated in an IC chip. This design was then implemented using a field-programmable gate array (FPGA) in which all-digital components were synchronized using the same on-chip clock to minimize phase noise. The FPGA chip used in this design is

*Address all correspondence to: Tao Wei, E-mail: tao_wei@uri.edu

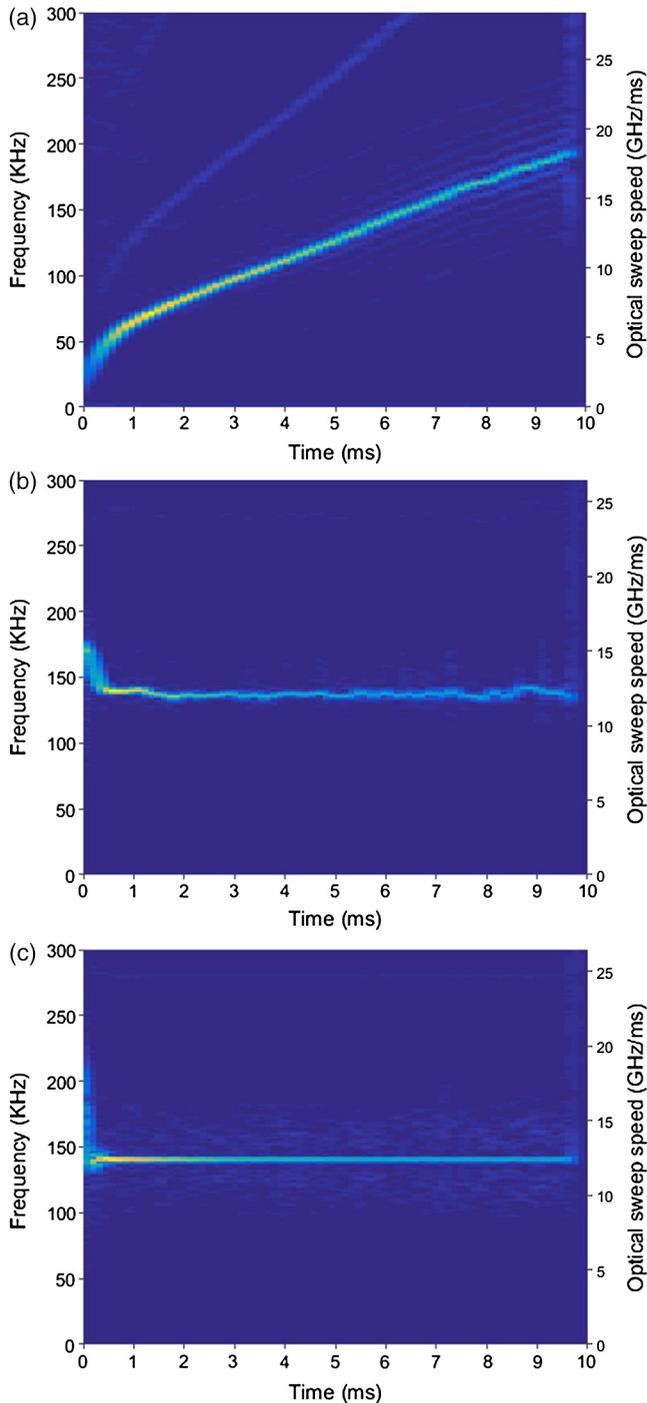


Fig. 2 Measured frequency spectrum of AGC output within a chirped laser pulse: (a) free running with ramp input, (b) free running with calibrated predistortion input, and (c) sweep velocity locked.

Along the sensing module, a homodyne interferometry structure is constructed using two 2×2 3-dB couplers as shown in Fig. 1. The input light is split into two paths via the first coupler, with one serving as the reference arm and the other path directed into the sensing arm, which includes a sub-THz-FS array. The sensing arm is terminated using an antireflection cut. The reflected light from the sub-THz-FS is then combined with light from the reference arm via the second coupler. A photodetector and a single channel AC-coupled 8-bit analog-to-digital converter (ADC) are

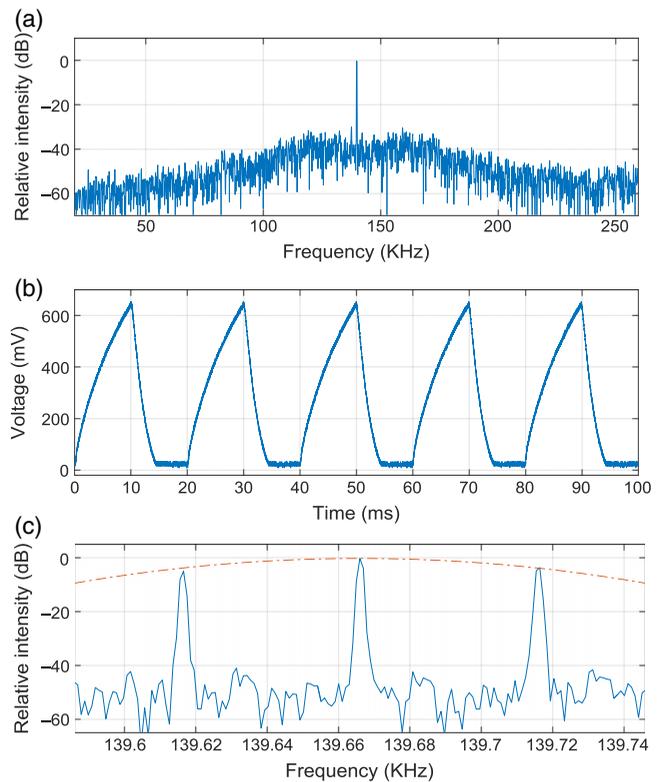


Fig. 3 (a) Fourier transform of the AGC output over the locked span of 9 ms, (b) output of the laser pulse generator with five complete cycles, (c) and a Gaussian curve fit applied to measure the FWHM of the Fourier transform of a chirped pulse train over 1 s.

used to record the resulting data. The sampling rate of the ADC is set to 8 MSa/s with a match antialiasing filter. The digitized raw data are then fed into a digital signal processing module.

3 Experimental Results

To investigate the sensing capability of the described SV-LLPG system, a 20-pt periodic weak reflection sub-THz-FS array with a 1-mm pitch length was fabricated along a single-mode fiber (SMF-28, Corning, Inc.) using a Ti:sapphire femtosecond laser micromachining system (Coherent, Inc.).^{6,7,25,26} During interrogation and signal processing, this sub-THz-FS array was considered to be nine cascaded sub-THz-grating sensor units using a 4-mm-wide moving Butterworth bandpass filter with a step size of 2 mm. Each sensor unit contains four reflection peaks. This signal processing method has been systematically investigated in the previous publications.^{7,27} A self-mixing method and a low pass filter are applied to extract the resulting interferograms. Changes in strain along the optical fiber result in optical path length changes among the weak reflectors, which generate a phase shift in the interferograms that are used to measure strain changes along the sensor probe.

To evaluate the strain sensing capability of the system, a series of static strain tests were conducted. One end of the fiber under test (FUT) was secured to an optical bench while the other end was left free to hang. Weights were sequentially added to the free end of the fiber at 1.33-g intervals; in total, 10.64 g of weights were added to the free end of the FUT, resulting in a strain change of $125.23 \mu\epsilon$. The

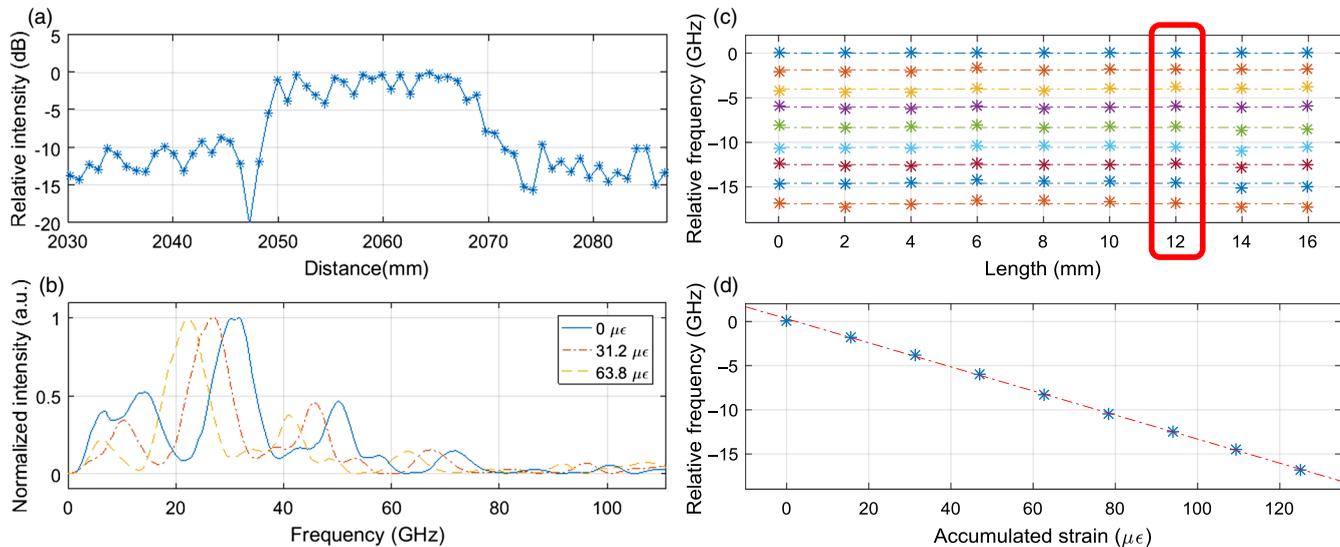


Fig. 4 Static strain test: (a) time domain reflections of DUT, (b) interferograms of the sensor unit between 1779 and 1783 mm with varied strain applied, (c) strain test results for all nine sensor units, and (d) strain test results for the seventh sensor unit.

SV-LLPG system was set using the parameters described above, resulting in a total sweeping bandwidth of 111.16 GHz. The resulting distance domain signals, calculated using a Fourier transform and in which the sensor structures can be identified between 2047 and 2067 mm, are plotted in Fig. 4(a). Due to the limited interrogation bandwidth, the individual reflection peaks of the sub-THz-FS array elements cannot be resolved. The measured frequency-domain interferograms of the seventh sensor unit between 2059 and 2063 mm are plotted in Fig. 4(b). The strain test results for all nine sensor units are plotted in Fig. 4(c), and the results of the seventh sensor unit specifically are plotted in Fig. 4(d). Linear results were observed for all sensor units, with the least linear having a R^2 value of 0.9986. The mean strain sensitivity across all sensing elements was calculated to be $-0.1346 \text{ GHz}/\mu\epsilon$ with a standard deviation of $0.0026 \mu\epsilon$. The start sweep frequency was evaluated by measuring the starting frequency of the entire system over 1000 captures, and the standard deviation of start frequency was 106.7 MHz.

4 Conclusions

This paper reports an FPGA-controlled SV-LLPG design. A DFB laser is employed as the sweep source and an ADPLL control system is used to lock the laser sweep velocity to an on-board reference clock. Highly linear chirped laser pulses with a bandwidth of 111.16 GHz were demonstrated. A sweep velocity of 12.35 GHz/ms was achieved for 9 ms within each chirped pulse at a 50-Hz pulse repetition rate. To investigate system sensing utility, the SV-LLPG prototype was used as an element of an OFDR system to interrogate a sub-THz-FS array. A static strain test was conducted and highly linear results were observed.

The proposed device holds the promise to deliver a low size, weight, and power and affordable interrogator for distributed fiber sensing applications. In addition, the FPGA-based design makes it easier to be integrated and adopted for various applications in the future.

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Zhen Chen received his BS degree in optics and optical science from Nanjing University of Science and Technology, Nanjing, China, in 2012 and his MS degree in electrical engineering from Hong Kong University of Science and Technology, Hong Kong, China, in 2013. He is currently a PhD candidate in the Department of Electrical, Computer, and Biomedical Engineering, University of Rhode Island, Kingston, Rhode Island. His research interests include instrumentation, fiber sensors, and laser wavelength modulation and control.

Gerald Hefferman received his BID degree from Pratt Institute, Brooklyn, New York, in 2009 and his diploma in premedical studies from the Harvard Extension School, Cambridge, Massachusetts, in 2013. He is currently pursuing an MD degree at the Alpert Medical School of Brown University, Providence, Rhode Island. Since 2013, he has been a research assistant at the University of Rhode Island, Kingston, Rhode Island. His research interests include the application of photonics to clinical medicine.

Tao Wei received his BS degree in mechanical engineering from Nanjing Technology University, Nanjing, China, in 2006 and his MS and PhD degrees in electrical engineering from Missouri University of Science and Technology, Rolla, Missouri, USA, in 2008 and 2011, respectively. He is currently an assistant professor of electrical engineering at the University of Rhode Island, Kingston, Rhode Island, USA. His research interests include photonic and microwave devices for various sensing applications.